

Automotive Electronics Council

Component Technical Committee

Agenda (subject to change)

2026 - First Asian
Automotive Electronics
Reliability Workshop

Hsinchu, Taiwan
Ambassador Hotel

Wednesday October 14, 2026

07:30 AM - 08:15 AM		Registration		
08:15 AM - 08:50 AM	O1.1	Welcome & AEC Updates	Rene Rongen	NXP Semiconductors
08:50 AM - 09:00 AM	O1.2	Host	Allan Tseng	Integrated Service Technology
09:00 AM - 10:30 AM	T1	Qualification & Functional Safety (1)	Moderator: Dave Tan	Integrated Service Technology
09:00 AM - 09:30 AM	T1.1	Impact of Radiation Effects on Advanced Process Semiconductors	Tseng, Wei	SGS Taiwan
09:30 AM - 10:00 AM	T1.2	Leveraging Functional Safety Analysis for Quality and Reliability	Melissa Uribe	Micron Technology
10:00 AM - 10:30 PM	W1	Q200	Christoph Raible / Rafael Monreal Vela	TDK Electronics / Murata
10:30 AM - 11:00 AM Break				
11:00 AM - 12:00 PM	T2	Problem Solving & Failure Analysis	Moderator: Rene Rongen	NXP Semiconductors
11:00 AM - 11:30 AM	T2.1	From Failure Localization to Zero Defect: A Problem-Solving-Driven Closed-Loop Framework Linking AEC-Q10X Qualification and AEC-Q004 Improvement	Graver Chang / Calvin Yang	Materials Analysis Technology Inc. / SGS Taiwan
11:30 AM - 12:00 PM	T2.2	Challenges to Failure Analysis for Secure Flash Memory Chip	John Grogan	Macronix
12:00 PM - 12:30 PM	W2	Q101	Corinne Bestory	Nexperia
12:30 PM - 2:00 PM Lunch				
02:00 PM - 02:45 PM	W3	WBG	Massimiliano Regardi	Nexperia
02:45 PM - 03:45 PM	T3	ESD & EOS	Moderator: John Grogan	Macronix
02:45 PM - 03:15 PM	T3.1	In-vehicle Networks System ESD Protection Design: ESD / EMC Solution and Latch-up Prevention	Amuro Chang	AMAZING Microelectronic
03:15 PM - 03:45 PM	T3.2	How promising is it to analyze every single EOS-like failure in depth?	Lorenzo Coceani / Kai Esmark	Infineon Technologies
03:45 PM - 04:15 PM Break				
04:15 PM - 04:45 PM	W4	Q102	Uwe Berger	Forvia
04:45 PM - 5:15 PM	T4	Statistical Techniques	Moderator: John Grogan	Macronix
04:45 PM - 5:15 PM	T4.1	Rare Event Maverick Limit Application	Larry Yeh	NXP Semiconductors
05:15 PM - 05:45 PM	W5	AEC External Cooperation / Chinese Standardization Development	Rene Rongen	NXP Semiconductors
05:45 PM Closure Day 1				

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Thursday October 15, 2026

08:15 AM - 08:30 AM	O2.1	Welcome	Rene Rongen	NXP Semiconductors
08:30 AM - 09:30 AM	W6	Q100	Ulrich Abelein	Infineon Technologies
09:30 AM - 10:30 AM	T5	Qualification & Functional Safety (2)	Moderator: Ulrich Abelein	Infineon Technologies
09:30 AM - 10:00 AM	T5.1	From Qualification Families to Knowledge Based Qualification: Enabling Qualification by Similarity	Kittikorn Kasuriya	NXP Semiconductors
10:00 AM - 10:30 AM	T5.2	System-Level HBM Reliability Screening via Pattern-Induced Thermal Cycling	Jinsoo (Ryan) Bae	Samsung Electronics
10:30 AM - 11:00 AM	Break			
11:00 AM - 11:15 AM	W7	Q006	Ulrich Abelein	Infineon Technologies
11:15 AM - 11:30 AM	W8	Q004	Rene Rongen	NXP Semiconductors
11:30 AM - 12:30 PM	T6	Zero Defects	Moderator: Calvin Yang	SGS Taiwan
11:30 AM - 12:00 PM	T6.1	Challenges of Measurable Deployment of AEC-Q004	Tseng, Wei	SGS Taiwan
12:00 PM - 12:30 PM	T6.2	Design-Phase prediction of ELFR assurance in automotive ICs using physics-based dielectric reliability modelling	Joao Goncalves / Max Stelzer	Infineon Technologies
12:30 PM - 02:00 PM	Lunch			
02:00 PM - 02:45 PM	W9	Q104	Steven Sibrel	Harman
02:45 PM - 03:45 PM	T7	Extended Mission Profiles	Moderator: Dave Tan	Integrated Service Technology
02:45 PM - 03:15 PM	T7.1	Impact of Prolonged 250°C Exposure for 2,000 Hours on CMOS Reliability	Ng Hong Seng	X-FAB
03:15 PM - 03:45 PM	T7.2	Tantalum Polymer Capacitors Trends and Challenges - Software Defined Vehicles (SDV) Performance and Miniaturization focused on Autonomous Driving and sensing Applications	Ken Lai	YAGEO Group / KEMET Electronics
03:45 PM - 04:15 PM	Break			
04:15 PM - 05:00 PM	W10	EMP	Rene Rongen	NXP Semiconductors
05:00 PM - 05:30 PM	W11	Q105	Steven Sibrel	Harman
05:30 PM - 05:45 PM	W12	CDC	Rene Rongen	NXP Semiconductors
05:45 PM	Closure Day 2 / Wrap-up			