

Automotive Electronics Council
Component Technical Committee

Tenth Annual Automotive Electronics Reliability Workshop

Call For Presentations

April 26 – 28, 2005

Sheraton Detroit Novi Hotel

21111 Haggerty Road
Novi, Michigan 48375
(248) 349-4000

Organized by:

AEC Component Technical Committee

Visteon Corporation
Delphi Electronics & Safety
Siemens VDO
Motorola Automotive
Johnson Controls

AMI Semiconductor
Advanced Micro Devices
Cirrus Logic
Freescale
International Rectifier

Infineon
Microchip
On Semiconductor
Renesas
STMicroelectronics

Temic
Texas Instruments
Tyco Electronics
Vishay
Xilinx

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This workshop provides a forum and structured environment to discuss quality and reliability problems and philosophies related to all aspects of passive component, discrete semiconductor, and integrated circuit design, test, analysis, fabrication, assembly and field performance. The environment is a highly interactive technical presentation and panel discussion format promoting open and frank communications within the international automotive component supplier base.

This call for presentations solicits your submission of abstracts of less than 200 words dealing with current automotive electronic component issues. The abstracts and topics will be reviewed and selected for presentation based upon technical merit, innovation, automotive focus and relevancy. ***Company commercials and/or endorsements are not allowed except for company IDs on presentation slide borders.*** Each presentation will be allotted 20-25 minutes, followed by a question and answer session. Presentations and late breaking contributions relevant to today's activities and tomorrow's ideas are requested.

Preferred "HOT" Topics:

Green/Pb-free component capability
Technical issues with 130nm and below
"Zero Defects" plans

bare die qualification/screen
OEM end customer requirements
Product/Process change control

PAT/SBL implementation
CDM repeatability

Examples of topics from past workshops:

Q100/Q101/Q200 Specification Impact
Maverick Lot/Wafer Programs
Component Family Qualification
Nominal Test Limits
Field Performance Data
Accelerated Stresses
ESD/EOS/EMI/Latch-up
Wafer Level Reliability
Performance Characterization

New Failure Mechanisms
Continuous Improvement
Known Good Die
Tri-Temp Testing
Quality Trends
Reliability Monitoring
Burn-In Elimination
Software Issues
Mission Profiles

Emerging Challenges
Preventive Failure Analysis
FMEA
New Test Techniques
MCM Technology
Flip Chip Technology
Advanced Packaging
QS/ISO-9000/9001
OEM System Reliability

Submit your abstract and finished presentation electronically in PDF format to:

Bob Knoell
Visteon Corporation
Office Phone: (313) 755-0162
Cell Phone: (586)260-7739
Email: rknoll@visteon.com

Key Presentation Dates

Abstract Submittal Deadline	March 11, 2001
Presentation Selection & Final Agenda	March 18, 2005
Final Copy of Presentation Due	April 15, 2005

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AEC Objective: The purpose of this workshop is to continuously improve communications between automotive electronic component users and their supplier base. The goal of the Automotive Electronics Council is the development of uniform automotive industry specifications and requirements to improve automotive product quality and reliability while reducing cost and time to market.

Who should attend: Engineers and managers who are involved in the qualification, testing, reliability, and failure analysis of automotive passive components, discrete semiconductor devices, and integrated circuit devices. It would also be helpful to those who are responsible for establishing quality and reliability policies for automotive components.

Registration Fee: There is **NO REGISTRATION FEE**, but workshop participants must register with the hotel before **April 4th, 2005** to ensure that the block room rate of \$129 per night is available. Traveling attendees are **HIGHLY ENCOURAGED to take advantage of this rate, as workshop costs are dependent on block usage.** This is your workshop. Please come with an open mind and be prepared to share and learn about new ideas and accomplishments. Your ideas on how to improve future workshops are always welcomed.

AEC Component Technical Committee

Sustaining Members:

Bob Knoell	Visteon	IC, discrete, passive	(313) 755-0162	rknoll@visteon.com
Detlef Griessman	Delphi	IC	(765) 451-9623	detlef.griessman@delphi.com
Brian Jendro	Siemens VDO	IC, discrete, passive	(256) 464-2980	brian.jendro.ext@siemens.com
Ken Kirby, Jr.	Visteon	passive	(313) 755-7935	kkirby10@visteon.com
Ron Haberl	Visteon	passive	(313) 755-5678	rhaberl@visteon.com
Mark Kelly	Delphi	IC	(765) 451-7084	mark.a.Kelly@delphi.com
Jeff Price	Delphi	discrete	(765) 451-1511	jeff.s.price@delphi.com
Bob Hulka, Jr.	Delphi	passive	(765) 451-1616	robert.s.hulka.jr@delphi.com
Erich Goertler	Siemens VDO	passive		erich.goertler@siemens.com
Jean Clarac	Siemens VDO	IC, discrete		jean.clarac@siemens.com

Associate members:

John Timms (john.timms/IC,discrete)	Roy Ozark (roy.ozark/passive)	Motorola Automotive (@motorola.com)
Michael Clark (michael.a2.clark)	Gary Fisher (gary.fisher)	Johnson Controls (@jci.com)
Daniel Vanderstraeten (daniel_vanderstraeten)		AMI Semiconductor (@amis.com)
David Lehtonen (david.lehtonen)		Advanced Micro Devices (@amd.com)
Xin Miao Zhao (xinmiao.zhao)		Cirrus Logic (@cirrus.com)
Nick Lycoudes (nick.lycoudes)		Freescale (@freescale.com)
Jeff Parker (jparker1)		International Rectifier (@irf.com)
Werner Kanert (werner.kanert)	Elfriede Geyer (elfriede.geyer)	Infineon (@infineon.com)
Mike Buzinski (mike.buzinski)		Microchip (@microchip.com)
Mark Gabrielle (mark.gabrielle)		On Semiconductor (@onsemi.com)
Ken Berry (kenneth.berry)		Renesas (@renesas.com)
Brian Mielewski (brian.mielewski)		STMicroelectronics (@st.com)
Wilhelm Mayer (wilhelm.mayer)		Temec (@temec.de)
Jo Wilson (j-wilson1)		Texas Instruments (@ti.com)
Lisa Jones (lajones)		Tyco Electronics (@tycoelectronics.com)
Ted Krueger (ted.krueger)		Vishay (@vishay.com)
Don Pecko (donald.pecko)		Xilinx (@xilinx.com)

Tentative Agenda

(subject to change based on abstract receipts)

Tuesday, April 26

7:30 am - 8:00 am Refreshments/Registration
8:00 am - 10:00 am **Session 1: Passive Components**
10:00 am - 10:30 am Break
10:30 am - 12:00 noon **Session 2: Passive Components**
12:00 noon - 1:30 pm Lunch (on own)
1:30 pm - 3:00 pm **Session 3: Passive Components / Discrete Semiconductors**
3:00 pm - 3:30 pm Break
3:30 pm - 5:00 pm **Session 4: Passive Components / Discrete Semiconductors**
Breakout Session
9:00 am - 5:00 pm **Tutorial: Qualification and MSL**
Bob Knoell (Visteon)
Nighttime Discussion
7:00 pm - 9:00 pm **AEC Panel Discussion: Q101 & Q200 Issues**

Wednesday, April 27

7:30 am - 8:00 am Refreshments/Registration
8:00 am - 10:00 am **Session 5: Discrete Semiconductors / Integrated Circuits**
10:00 am - 10:30 am Break
10:30 am - 12:00 noon **Session 6: Discrete Semiconductors / Integrated Circuits**
12:00 noon - 1:30 pm Welcome & Awards Luncheon
1:30 pm - 3:00 pm **Session 7: Integrated Circuits**
3:00 pm - 3:30 pm Break
3:30 pm - 5:00 pm **Session 8: Integrated Circuits**
Nighttime Discussion
7:00 pm - 9:00 pm **AEC Panel Discussion: Q100 Issues**

Thursday, April 28

7:30 am - 8:00 am Refreshments/Registration
8:00 am - 10:00 am **Session 9: Integrated Circuits**
10:00 am - 10:30 am Break
10:30 am - 12:00 noon **Session 10: Integrated Circuits**
12:00 noon - 1:30 pm Lunch (on own)
1:30 pm - 3:00 pm **Session 11: Integrated Circuits**
3:00 pm - 3:30 pm Break
3:30 pm - 4:30 pm **Session 12: Integrated Circuits**
4:30 pm - 5:00 pm Closing Remarks

Key Workshop Dates

Preliminary Call for Presentations	January 19, 2005
Abstract Submittal Deadline	March 11, 2005
Presentation Selection & Final Agenda	March 18, 2005
Hotel Reservation Deadline *	April 4, 2005
Workshop Registration Closes **	April 15, 2005
Final Copy of Presentation Due	April 15, 2005
Workshop Begins	April 26, 2005

* *Registration & Hotel information provided at a later date*

** *Walk-ins welcome pending space availability*

Tape recorders & cameras are prohibited at the workshop